

# BIO-DATA

## Dr. K SRILAKSHMI

Department: Electronics and Communication  
Sheshadri Rao Gudlavalleru Engineering College

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### Address for Communication

H.No:1-156,

Mudinepalli (PO &Mandal),

Krishna District,

Andhra Pradesh, PIN: 521325.

### Academic Qualifications

S. No	Name of the Degree (Starting from Ph.D to 10 <sup>th</sup> Class)	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	Ph.D	JNTUH, Hyderabad	---	Low Power VLSI Design	January 2022
2	M.Tech	JNTUK, Kakinada	84.42%	VLSISD	2011
3	B.Tech	JNTUK, Kakinada	76.58%	ECE	2009
4	Diploma	S.B.T.E.T. Hyderabad	81.9%	DECE	1993
5	SSC	BSE, Hyderabad	66.6%	-	1989

## Professional Experience

S. No	Designation	Institution Name	Working Period	
			From	To
1	Associate Professor	SRGEC, Gudlavalleru	01.07.23	Till Date
2	Assistant Professor	GEC, Gudlavalleru	01.10.2010	30.06.23

## Professional Body Memberships

S. No	Name of the Professional body	Membership Number
1	IE(I)	AM162848-0

## Papers Published in Journals: 21

1. K Srilakshmi et al., "Performance comparison of FinFET based energy recovery adder circuits", Telecommunications and radio engineering, Vol. 83 (5), pp. 83-94, 2024. DOI: 10.1615/TelecomRadEng.v83.i5.80.
2. K Srilakshmi et al., "HDL implementation for real-time image properties adjustments", International Research Journal of Engineering and Technology (IRJET), Volume: 11 Issue: 04, April 2024.
3. K Srilakshmi et al., "Design and Analysis of Ultra Low-Power Approximate Adder", International Research Journal of Modernization in Engineering Technology and Science, Vol. 5(4), April 2023, <https://www.doi.org/10.56726/IRJMETS35834>.
4. K Srilakshmi et al., "Ultra-Low Power Approximate Multipliers using Energy Recovery Logic", International Journal of Novel Research and Development (IJNRD)", Vol. 8(4), April 2023.
5. Y. Syamala and K. Srilakshmi, "Adiabatic Multiplexer and Delay Flip-Flop", Springer book series-Advances in electrical and computer Technologies, Vol. 884, 2022, pp. 715-725.
6. Senthil Kumar Kandasamy, S Maheswaran, K R Kavitha, D Karthikeyan, J Indra, K Sheikdavood, Y Syamala, K Srilakshmi, Chandrasekaran Arumugam, Dhivya Balamoorthy, "High-Performance Graphene Oxide/Polypyrrole/Ziziphus jujuba/Prunus dulcis Ternary Composite Electrodes for Super capacitor for Sensor Applications" Hindwai Journal of Nano materials" Volume 2022, Article ID 9201910, <https://doi.org/10.1155/2022/9201910> 2022.
7. Srilakshmi Kaza , Syamala Yarlagaadda , Venkata N Tilak Alapati , Srinivasa Rao Kunupalli, E. Vijaya Babu," Performance Analysis of Adiabatic Vedic Multipliers" Turkish Journal of Computer and Mathematics Education, Vol. 12(5), 2021, pp. 1429-1435.
8. K Srilakshmi, A V N Tilak, K Srinivasa Rao and Y Syamala, "Energy Efficient 64-bit Asynchrobatic Adder", Book chapter, Lecture notes in electrical engineering book series(LNEE), Vol. 476, 2018, pp. 499-508.
9. K Srilakshmi, A V N Tilak and K Srinivasa Rao, "Ultralow-Power and Secure S-Box

- Circuit Using FinFET Based ECRL Adiabatic Logic”, Journal of Science and Technology, Vol. 10 No. 3 (2018) p. 10-17.
10. Jayaram Bevara, Tilak VN Alapati, Srilakshmi Kaza, “Power Optimized Dadda Multiplier Using Two-Phase Clocking Sub-threshold Adiabatic Logic”, International Journal of Electronics Engineering Research, Vol.9 (8), 2017, pp. 1171-1184.
  11. Nekkanti Gothami and K Srilakshmi, “Design and Implementation of Reversible Multiplier using optimum TG Full Adder”, IOSR Journal of Electronics and Communication Engineering (IOSR-JECE), Vol. 12(3), June 2017, pp. 81-89.
  12. T Durga prasad, K Srilakshmi and Y Syamala, “Design and implementation of energy efficient code converters”, International Journal of computer science and information technology & Security, Vol. 6(4), pp. 33-36, 2016.
  13. M Uma Maheswari and K. Srilakshmi “Power Gating Based Ground Bounce Noise Reduction”, International Journal of Engineering Research and Applications (IJERA), Vol. 4(8), 2014, pp: 54-60.
  14. K Srilakshmi, Y Syamala and A Suvir vikram, “Design and Implementation of CMOS VLSI Circuits using Dual Subthreshold Supply Voltages”, International Journal of Engineering Research and Applications (IJERA), Vol. 3(5), 2013, pp: 1604-1608.
  15. N Somasehara varma, Y Syamala and K Srilakshmi, “Design of low power logic circuits using gate diffusion input(GDI) technique”, International journal of VLSI design and communication systems (VLSICS), Vol. 4(5), 2013, pp: 89-95.
  16. A Suvir Vikram, K Srilakshmi and Y Syamala, “Static Power Optimization Using Dual Sub Threshold Supply Voltages In Digital CMOS VLSI Circuits”, International journal of VLSI design and communication systems (VLSICS), Vol. 4(5), 2013, pp: 77-88.
  17. Y Syamala, K Srilakshmi and N Somasekhar Varma, “Design and Implementation of CMOS VLSI Digital Circuits Using Self-Adjustable Voltage Level Technique” International Journal of Engineering Research and Applications (IJERA), Vol. 3(5), 2013, pp: 1941-1946.
  18. Y Syamala, A V N Tilak and K Srilakshmi, “Testing of Reversible Combinational Circuits”, , Springer LNICST 108, pp. 46–53, 2012.
  19. G Bindu and K Srilakshmi, “A Novel Approach of LSB Steganography for Retrieving Text from Audio”, International Journal Computer Technology & Applications (IJCTA), Vol. 3(4), 2012, pp: 1384-1387.
  20. M V Narayana, G Ashok, Y Syamala and K Srilakshmi, “Low Power CMOS Digital Design Using Adiabatic Logic”, Proceedings of ICVSP.
  21. K Srilakshmi, K Anitha, Dr. P Rajesh Kumar and G R L V N Srinivas Raju, “Design and implementation of distance measuring digital hardware”, IJECT, Vol. 2(1), 2011, pp: 222-225.

### **Papers presented in Conferences: 15**

1. K Srilakshmi et al., “Performance analysis of Approximate Circuits”, Proc. Of One-Day Seminar on Technology Advances in Smart System Development (OSTASSD-2024), 16<sup>th</sup> November 2024.

2. Srilakshmi Kaza, Syamala Yarlagadda and Maheswaran S, "Ultra Low power Approximate Arithmetic Circuits", Proc. Of 14<sup>th</sup> International Conference on Computing, Communication and Networking Technologies", (ICCCNT), July 6<sup>th</sup> – 8<sup>th</sup> 2023, IIT Delhi.
- 3.Y. Syamala and K. Srilakshmi, "Adiabatic Multiplexer and Delay Flip-Flop", Proc of International Conference on Advances in Electrical and Computer Technologies" pp.715–725, 2021.
- 4.K. Srilakshmi, A V N Tilak, K. Srinivasa Rao and Y. Syamala, "Secured MPFAL Logic for IoT Applications", Proc. of 2<sup>nd</sup> International conference on Device, Circuit and System (2020 IEEE VLSI DCS), July 18-19, 2020.
5. K Srilakshmi, A V N Tilak and K. Srinivasa Rao, " Energy Efficient Adder for Bio-Medical Applications" Proc. of IEEE Region 10 international conference on Humanitarian Technology (R10HTC), December 2018,Srilanka, pp. 1-5. **(Best Paper Award)**.
6. K. Srilakshmi, A. V. N Tilak and Y. Syamala, " Development of Low-Power VLSI Circuits for Biomedical Applications" Proceedings of First International Conference on Computational and Intelligent Techniques for Automation of Engineering Systems (CITAES-2018), November 30 & December 1, pp. 23-27, 2018.
7. E. Vijaya Babu, Y. Syamala, K. Srilakshmi and K. Siva Anjaneyulu, "Design and analysis of multiplexer based flash ADC", Proc. of CITAES 2018.
8. K. Srilakshmi, A. V. N. Tilak and K. Srinivasa Rao, "Ultralow-Power Multiplier for DSP Applications", Proc. of Seventh International Conference on Advances in Computer Science and Application – CSA 2018 and to be published in IJEECSE.
9. Y. Syamala, A. V. N. Tilak, K. Srilakshmi and T. Anil Chowdary, "Low Power Testable Reversible Combinational Circuits", IEEE international conference on Intelligent Computing and Control Systems (ICICCS), June 14-15, 2018, Madurai.
10. K. Srilakshmi, A. V. N. Tilak and K. Srinivasa Rao, "Performance of FinFET Based Adiabatic Logic Circuits" , Proc. of IEEE International Conference (TENCON-16), Singapore, pp. 2377-2382, 2016.
11. K. Srilakshmi, A. V. N Tilak, K. Srinivasa Rao and Y. Syamala, "Energy Efficient 64-Bit Asynchrobatic Adder" , Proc. of International Conference on Nano-electronics, Circuits and Communication Systems (NCCS-2016).
12. B N V Amar Surendra Babu, Y Syamala and K Srilakshmi, "Design and Implementation of Power Optimized 64 Bit Floating Point ALU Employing Block Enabling Technique", 4th International Conference on Innovations in Electronics & Communication Engineering, Hyderabad during 21-22 August 2015.
13. Y Syamala, AVN Tilak, K Srilakshmi, "Testing of Reversible Combinational Circuits", Third International Conference on Advances in Communication, network an computing (CNC-2012), Channai, During 24<sup>th</sup> -25<sup>th</sup> Feb 2012.
14. Y.Syamala and K.Srilakshmi B.Kavyasree, "Transistor Realization of Reversible parallel Adder/subtractor", National Conference on VLSI, Signal Processing and Communications, Vadlamudi, During 5<sup>th</sup> -6<sup>th</sup> Feb 2012.
15. G.R.L.V.N. Srinivas Raju K. Srilakshmi, K. Anitha, Dr P. Rajesh Kumar, "Design and implementation of distance measuring digital hardware", International Conference on Advances in Computer Science, Communication and Bio-Instrumentation Engineering (ICACCBIE-2011), Eluru, During 8<sup>th</sup> -9<sup>th</sup> Dec 2011.

## Patents:

1. Seshadri Rao Gudlavalleru Engineering College, Dr. Y Syamala, Dr. V V K D V Prasad, Dr. K Srilakshmi, "LoRAWAN based Smart Agricultural Monitoring system with Artificial Intelligence Analysis" 2022, Patent Office Journal No. 34/2022, Dated 26/08/2022. (Published), (Under examination).
2. Seshadri Rao Gudlavalleru Engineering College, Dr. K. Sri Lakshmi, Dr. T. Ananda Babu, Mr. G. Ramakrishna, Mr. B. Vamsy Krishna, Mr. P. Anil Kumar, "Integrated Alert System with Wireless Network and Method Thereof:" Patent Office Journal No. 37/2022 Dated 16/09/2022, pp. 58405. (Published).
3. K Srilakshmi et al., "IOT Based Smart Home Model for Assisting Elderly People" Published date:08/07/2022, German patent granted.
4. Patent Application Publication, "Magnetic Resonance Coupling Wireless Power Transfer Unit For Implantable Biomedical Devices", The Patent Office Journal No. 34/2020, Dated :21/08/2020, pp. 33088.

## Workshops /Conferences /Seminars Attended No. : 20

1. One-Day Seminar on Technology Advances for Smart Systems Development-2024(OSTASSD – 2024), organized by Institution of Engineers India (IEI) in association with Prasad V. Potluri Siddhartha Institute of Technology, Vijayawada on 16<sup>th</sup> November 2024.
2. 3<sup>rd</sup> one week workshop on VLSI Device, Circuit and System Design Tools" Organized by school of electronics engineering, VIT-AP University, Amaravati(AP), during 27<sup>th</sup> June-3<sup>rd</sup> July 2023.
3. One day workshop on "Scholarly Communication", organized by Research & Development cell, GEC Gudlavalleru on 25<sup>th</sup> February 2021.
4. Workshop on "Reference Management tools and Online Citation Database" with grade A , organised by Indian Academic Researches Association, during 21<sup>st</sup> to 23<sup>rd</sup> May 2020.
5. Attended workshop on "VLSI circuits Analog and Digital Design perspectives", organised by Department of Electronics and Communication Engineering JNTUA college of Anantapuramu in association with AMS Semiconductors and HCL Technologies during 16<sup>th</sup> - 17<sup>th</sup> May 2020.
6. Attended a two day online workshop on " **Modern Methods for Teaching-Learning Practises**", organised by Krishna University on 12<sup>th</sup>-13<sup>th</sup> May 2020.
7. Three day workshop on "Discovery of people reaction and needs after disaster using data analysis techniques" organized by IEEE Region 10 international conference on Humanitarian Technology (R10HTC) during 6<sup>th</sup>-8<sup>th</sup> December 2018in Colombo, Sri Lanka.
8. Three day workshop on "Co-designing primary health care platforms for resource constrained environments" organized by IEEE Region 10 international conference on Humanitarian Technology (R10HTC) during 6<sup>th</sup>-8<sup>th</sup> December 2018in Colombo, Sri Lanka.
9. One week workshop on "CMOS, Mixed signal and radio frequency VLSI Design" organized by IIT Kharagpur from 30<sup>th</sup> January-4<sup>th</sup> February 2017.
10. Two day "workshop on Analog VLSI Design" covering OPAMP design, PLL Design, data converters, RFCMOS and analog Fault Diagnosis organized by C-DAC Bangalore during 22-23<sup>rd</sup> January 2016.

11. One day workshop on “Ultra Low Power Communication using CC430 Microcontroller:: Hands on Experience” Conducted by Signal processing and Communication Research Group, Department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 7<sup>th</sup> Feb 2015.
12. Two Week ISTE workshop on “Signals & Systems” conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 2<sup>nd</sup> -12<sup>th</sup> January 2014.
13. One week short term course on “Advances in VLSI Signal Processing” conducted by IIT Kharagpur during 03<sup>rd</sup> -07<sup>th</sup> Dec 2013.
14. Two week ISTE workshop on “Analog Electronics” Conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 4<sup>th</sup> -14<sup>th</sup> June 2013.
15. One week workshop on “Training Teachers for Excellence” conducted by Gudlavalleru Engineering College, Gudlavalleru during 29<sup>th</sup> April - 4<sup>th</sup> May 2013.
16. Two day national workshop “Advanced VLSI Methodology (NW-AVLSIT-2013)” conducted by department of ECE, University College of engineering, JNTUK, Kakinada during 26<sup>th</sup> -27<sup>th</sup> April 2013.
17. Two day ISTE workshop on Akash for Education conducted by IIT Bombay in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 10<sup>th</sup> - 11<sup>th</sup> November 2012.
18. Participated in the International Student Conference On VLSI Design, Hyderabad International Convention Centre (HICC), Hyderabad during 9<sup>th</sup> -10<sup>th</sup> Jan 2012.
19. One day workshop on “TI-Digital Signal Processors and their Applications” conducted by Shri Vishnu Engineering College for Women during 10<sup>th</sup> Feb 2011.
20. Two day workshop on “Digital Design Simplified - Taking Design from Simulation to Layout Extraction” conducted by Shri Vishnu Engineering College for Women during 30<sup>th</sup> April-1<sup>st</sup> May 2010.

### **Certifications/Training Programs Attended No. : 40**

1. Dr. K Srilakshmi: 5-Day Faculty Development Programme entitled “Deep Dive in to IOT Technologies and Applications” organized by Dept. of ECE, LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING, Mylavaram from 24<sup>th</sup> to 28<sup>th</sup> June 2024.
2. Dr. K Srilakshmi: One Week Faculty Development Programme on “Emerging Trends and Developments in Simulation and Fabrication of Semiconductor Devices (online)” organized by the School of Electronics Engineering at VIT-AP University, Amaravati, during 2<sup>nd</sup> -8<sup>th</sup> May 2024.
3. Dr. K Srilakshmi: Participated Professional development program on “Verilog Programming” Conducted by NITTTR, Chennai in online mode from 29<sup>th</sup> April to 3<sup>rd</sup> May 2024.
4. Dr. K Srilakshmi: One week national level FDP on “Outcome based education and Application of Generative AI in teaching and research” organised by the Internal Quality Assurance Cell (IQAC) of Gauhati University, Assam in association with ipsr solutions limited from 17<sup>th</sup> to 24 April 2024.
5. A two day FDP on “Design of Integrated Circuits using Microwind Tools”, Organized by Dept. of ECE, SRGEC, from 30<sup>th</sup>-31<sup>st</sup> August 2023.

6. A one week International Faculty Development Program on "Internet of Things, Industry 4.0 and Stress Management" Organized by the dept. of Internet of Things, SRGEC in association with IETE Vijayawada center from 17th -22nd July 2023.
7. Five days National Level FDP on "Recent trends in Machine Learning and Artificial Intelligence for VLSI", Organized by dept. of ECE, CMR Institute of Technology Bengaluru during 29th May-2nd June 2023.
8. Blended mode professional development program on "Outcome based education and design thinking" organized by IEEE Guntur subsection and department of Electrical and Electronics Engineering, KLEF from 7th -15th October 2022.
9. One week National level online faculty development program on "Trends in Embedded systems and IoT", Organized by ECE dept of SRGEC during 20th -25th June 2022.
10. Two day faculty training program on "Design and development of VLSI circuits using Mentor Graphics Tools", Organized by Dept. of ECE, GEC during 14<sup>th</sup> -15<sup>th</sup> December 2020.
11. AICTE sponsored online Short Term Training program on Block chain Architecture and use cases Phase I organised by department of electronics and communication engineering, Gudlavalleru engineering college during 10<sup>th</sup> -15<sup>th</sup> August 2020.
12. One week refresher course on Embedded system design organised by department of electronics and communication engineering, Gudlavalleru engineering college during 27<sup>th</sup> July-1<sup>st</sup> August 2020.
13. Completed APSSDC online FDP on Internet of Things during 13<sup>th</sup> -25<sup>th</sup> July 2020.
14. National level online FDP on Cyber Security, organised by department of CSE and IT in association with Cyberpsy of gudlavalleru engineering college during 22<sup>nd</sup> -26<sup>th</sup> July 2020.
15. Attended Five day online Faculty Development Programme on "**Opportunities and Challenges in Next-Generation Semiconductor Devices**", organised by Department of Electronics and Communication Engineering, Anil Neerukonda Institute of Technology and Sciences, during 16<sup>th</sup> - 20<sup>th</sup> June 2020.
16. Participated in Virtual Book Launch and expert talks on "**Future of Higher Education – Nine Mega Trends**", organised by ICT Academy on 30<sup>th</sup> June 2020.
17. Attended Online Faculty Development Programme on "**MATLAB and Simulink in Engineering Education**", organised by APSSDC during 10<sup>th</sup> -11<sup>th</sup> June 2020.
18. One week international online Knowledge development program on "**challenges and advancements in the design of IOT, VLSI and Embedded Systems: A Researcher View**", organised by Department of Electronics and Communication Engineering of Gudlavalleru Engineering College during 8<sup>th</sup> - 13<sup>th</sup> June 2020.
19. One week online Faculty Development programme on "**Electronics and Communication Engineering Trends and Research Areas in Applied VLSI and Advanced Communicatons**", organised by Department of Electronics and Communication Engineering, Vasireddy Venkatadri Institute of Technology during 8<sup>th</sup> - 10<sup>th</sup> June 2020.
20. One week online faculty Development Program on "**Artificial Intelligence and its Applications**", organised by Department of Computer Science and Engineering in collaboration with 360DigiTMG, Ramachandra College of Engineering During 25<sup>th</sup> - 30<sup>th</sup> May 2020.

21. Attended Five day online Faculty Development Program on “**Trends in Communication**”, organised by the Department of Electronics and Communication Engineering in association with Averzs Technologies, Sir. C R Reddy College of Engineering during 26<sup>th</sup> - 30<sup>th</sup> May 2020.
22. Participated and successfully completed three days online course on “**Writing Case Studies, Project Preparation and Funding of Research Projects**”, with grade A , organised by Indian Academic Researches Association, during 21<sup>st</sup>- 23<sup>rd</sup> May 2020.
23. Participated in season 5 sessions organised by ICT Academy skycampus on the theme “**The Future of Skills –Education, Employment and Entrepreneurship**”, conducted on 11<sup>th</sup> -15<sup>th</sup> May 2020.
24. Successfully completed Online Certification Skill Development Program on “**Design Implementation and Verification in VLSI**”, organised by Sandeepani School of Embedded System Design, during 27<sup>th</sup> April - 1<sup>st</sup> May 2020.
25. Successfully Completed online Faculty Program on “**NBA**”, with a score of **65%**, organized by Bharati Vidyapeeth College of Engineering on 12<sup>th</sup> May 2020.
26. One week FDP on “Data Acquisition and RT using LabVIEW”, Organized by Dept. of ECE, GEC in association with National Instruments, Bangalore during 25<sup>th</sup> -30<sup>th</sup> November 2019.
27. Three day training program on “Advanced Development Tools based IoT” organized by dept. of ECE, GEC Gudlavalleru during 11<sup>th</sup> -13<sup>th</sup> November 2019.
28. One week faculty development program on “Communications and signal processing” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29<sup>th</sup> October to 3<sup>rd</sup> November 2018.
29. Two day training program on “Mentor Graphics Tools” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 31<sup>st</sup> August and 1<sup>st</sup> September 2017.
30. One week faculty development program on “Hands on Experience in signal processing Applications” organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru During 7-12 November 2016.
31. Two day staff training program on "Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 4-6 November 2016.
32. One week refresher course on “Microprocessors & Microcontrollers” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during held during 17-21 November 2015.
33. Two day training program on “Analog and Digital CMOS IC Design flow using Mentor graphics EDA Tools” Department of ECE, Shri Vishnu Engineering College for Women, Bhimavaram from 5<sup>th</sup> -6<sup>th</sup> October, 2015.
34. One day seminar on “Research in Signal and Image Processing on FPGAs and Embedded platforms” The Institution of Engineers(India), Vijayawada Local Center, Vijayawada on 22<sup>nd</sup> Nov 14.
35. One day workshop on “Speech Processing: Current Challenges and Hands-on Experience” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23<sup>rd</sup> August 2014.
36. Attended 3<sup>rd</sup> Research methodology course conducted by JNTUH, Hyderabad during



16<sup>th</sup>-21<sup>st</sup> December 2013.

37. One week refresher course on “Electronic Devices and circuits” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 23 – 28<sup>th</sup> September 2013.
38. Attended the complimentary training on “MATLAB, Simulink & Related Tool Boxes for Engineering Education” Organised by GEC, Gudlavalleru during 3<sup>rd</sup> -4<sup>th</sup> April 2013.
39. Faculty Development Program on “VLSI and Embedded System Design” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 7<sup>th</sup> – 8<sup>th</sup> July 2012.
40. One week refresher course on “Pulse & Digital Circuits (Theory & Lab Practice)” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21<sup>st</sup> – 26<sup>th</sup> November 2011.

#### **NPTEL Courses Completed:**

1. Microprocessors and Microcontrollers with a score of 88 top 2%
2. Hardware Modeling Using Verilog with a score of 90 top 5%
3. Digital Circuits with a score of 90 top 1%
4. CMOS Digital VLSI design with a score of 73
5. Digital Integrated circuits
6. Digital System Design with Top 5% grade

Dr. K Srilakshmi: successfully completed and received a passing grade in IOT2x: IoT Sensors and Devices a course of study offered by CurtinX, an online learning initiative of Curtin University. 24<sup>th</sup> May 2024.

. K Srilakshmi: successfully completed and received a passing grade IOT1x: Introduction to the Internet of Things (IoT) a course of study offered by CurtinX, an online learning initiative of Curtin University. 10<sup>th</sup> June 2024.

Dr. K Srilakshmi: successfully completed and received a passing grade IoT101: Build Your First IoT Application with Arma course of study offered by Arm EducationX, an online learning initiative of Arm Education. 18<sup>th</sup> June 2024.

Dr. K Srilakshmi: successfully completed and received a passing grade RFPF101: Programming 101 - An Introduction to Python for Educators a course of study offered by Raspberry Pi Foundation, an online learning initiative of Raspberry Pi Foundation. 25<sup>th</sup> June 2024.

#### **R&D and Consultancy:**

##### **External Funded Projects**

1. UGC Minor Research project titled “A Novel Approach for Development of Low Power VLSI Circuits for Energy Efficient Pacemakers” Grant Amount: Rs. 3, 40,000/- completed as a Principal Investigator during 2015-2017.

##### **Guest Lectures Delivered**

1. Delivered a guest lecture on “DigitALL: Innovation technology for gender equality” on the occasion of International Women's Day Celebrations organized by Akkineni Nageswara Rao College, Gudivada on 6.3.23.

2. Delivered a lecture on “CPU and Micro programmed control” as a part of Computer organization orientation program to faculty of ECE during 23<sup>rd</sup> and 24<sup>th</sup> February 2023.
3. Delivered a lecture on the topic “Architecture and Instruction Set of 8086 Microprocessor” as a part of one week refresher course on “Microprocessors & Microcontrollers” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 17<sup>th</sup>-21<sup>st</sup> November 2015.
4. Delivered a lecture on the topic “VHDL Design flow” as a part of one week refresher course on “Digital system design and digital IC applications” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 17<sup>th</sup>-21<sup>st</sup> June 2014.

### **Tools Expertise**

1. Cadence Virtuoso
2. Mentor Graphics frontend and Backend
3. Multisim
4. Proteous

### **Labs Development**

1. Guided as a mentor for the development of Analog Electronics virtual lab in association with IIT Roorkee.

### **Course Materials Prepared**

1. Universal Human Values
2. CAD For VLSI
3. VLSI Design
4. CMOS Digital IC Design
5. Low Power VLSI Circuits
6. ASIC Design
7. Microprocessors and Microcontrollers
8. Microcontrollres and Embedded Systems

### **Additional Works**

1. Microprocessors and Microcontrollers lab Incharge.
2. Internal examinations in-charge at institution level since 2018.
3. Coordinator for department NBA works.
4. Department examination committee in-charge for several years.
5. Board of Studies Member
6. Department over all counseling coordinator.

## **Declaration**

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date:

Signature